

APT

A tool for analysing and synthesising Petri nets and transition systems

Eike Best and Uli Schlachter

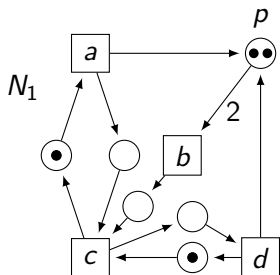
`{eike.best,uli.schlachter}@informatik.uni-oldenburg.de`

Carl von Ossietzky Universität Oldenburg

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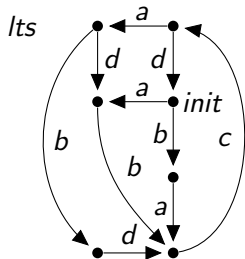
Petri nets and transition systems



Petri net

analysis \rightarrow

\leftarrow synthesis



state space
reachability graph
transition system

- versatility

- versatility
- transparency

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- extensibility

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- modularity

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- portability
- availability

APT

The team



12 students, 4 supervisors, one year project

APT

The tool

- Java 7
- Single JAR file
- Command line
- Non-interactive
- About 80 different modules
- Each module solves one specific problem
e.g. “compute the reachability graph of a given Petri net”

Demonstration

- Structural properties

- ▶ plain
- ▶ pure
- ▶ connectedness
- ▶ marked graph/T-net
- ▶ output-nonbranching
- ▶ conflict-free
- ▶ S- and T-invariants
- ▶ traps and siphons

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- Behavioral properties

- ▶ reachability graph
- ▶ coverability graph
- ▶ k -boundedness
- ▶ (weak) liveness
- ▶ word in language
- ▶ behavioural conflict free
- ▶ binary conflict free
- ▶ properties of the reachability graph

- Structural properties

- ▶ determinism
- ▶ total reachability
- ▶ persistency
- ▶ reversibility
- ▶ connectedness
- ▶ weak/strong separability

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- ▶ language equivalence
- ▶ bisimulation
- ▶ isomorphism

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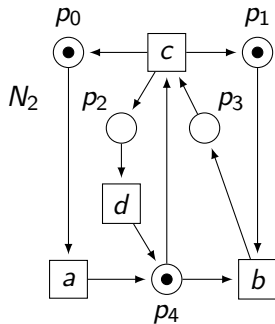
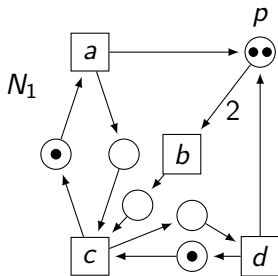
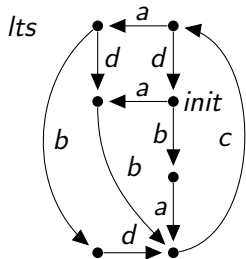
- ▶ language equivalence
- ▶ isomorphism
- ▶ bisimulation

- PN-Synthesis

- ▶ Find Petri net with isomorphic reachability graph
- ▶ Find Petri net with language-equivalent reachability graph

What is Petri net synthesis?

An Its and two different Petri nets solving it:



Petri net synthesis

- Solve Lts with Petri net (isomorphic / language equivalent)
- Free Petri nets: No two transitions generate same letter

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- Free Petri nets: No two transitions generate same letter
- Restrict the net to combination of:
 - ▶ pure
 - ▶ plain
 - ▶ output-nonbranching
 - ▶ t-net
 - ▶ conflict-free
 - ▶ k-bounded
 - ▶ safe
- Distributed Petri nets:
 - ▶ Assign locations to letters
 - ▶ Letters with different location do not have “shared state”:
disjoint presets

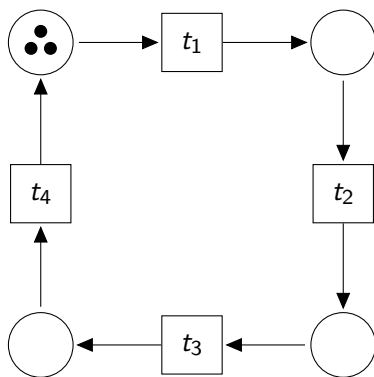
- Compare APT with petrify¹, synet² and GENET³
- Approach:
 - ▶ Generate Petri net of size n
 - ▶ Calculate reachability graph with APT
 - ▶ Measure: Synthesize pn
- Specs: Intel Core i7-4790 with 3.6GHz, 32 GiB of RAM

¹<http://www.cs.upc.edu/~jordicf/petrify/>

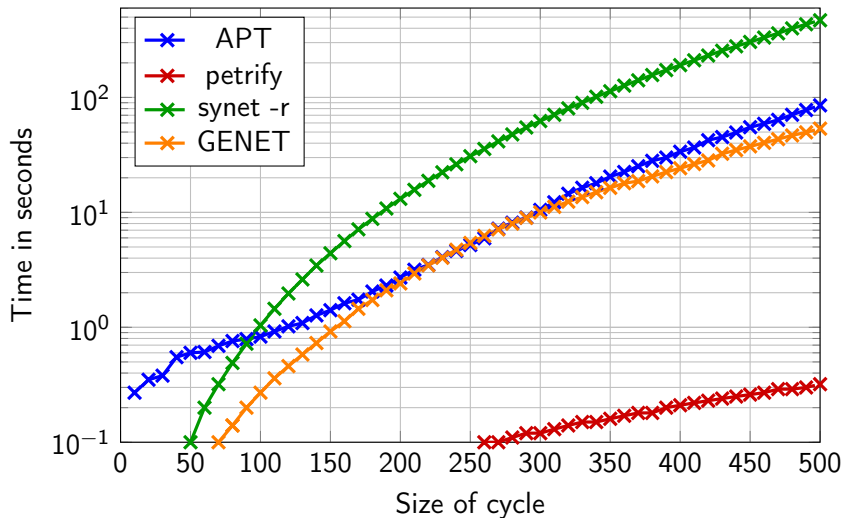
²<https://www.irisa.fr/s4/tools/synet/>

³<http://www.cs.upc.edu/~jcarmona/genet.html>

Cycle of size $n = 4$ with $k = 3$ token

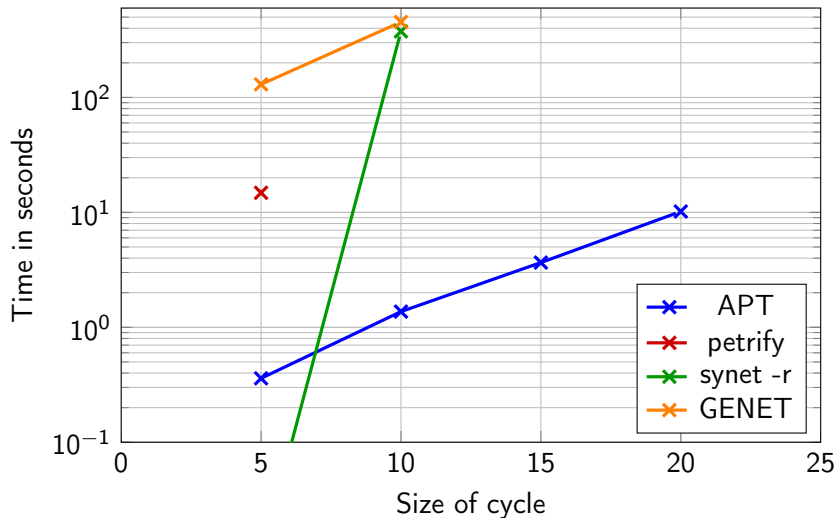


Synthesizing cycles with $k = 1$ tokens⁴: Run time



⁴Upper time limit: 600 sec

Synthesizing cycles with $k = 5$ tokens⁵: Run time



⁵Upper time limit: 600 sec

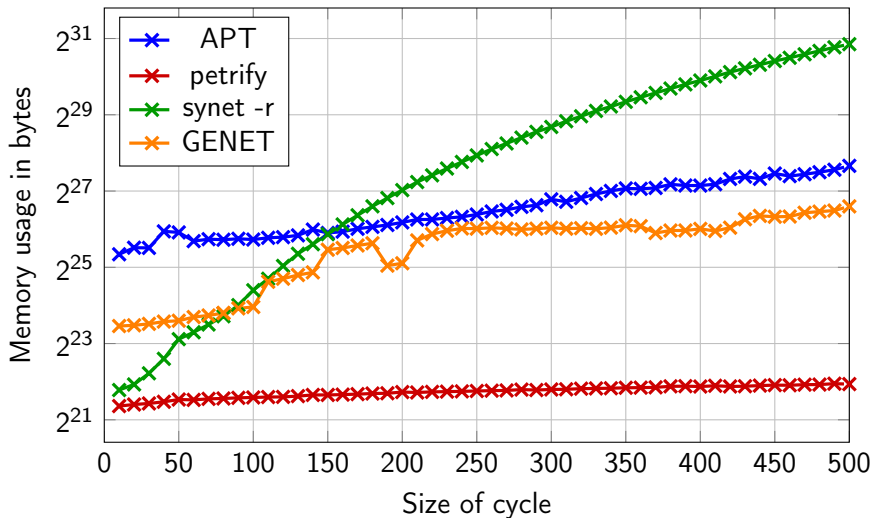
What's next?

- Get APT on <http://github.com/Cv0-theory/apt>
- Toolbox containing many algorithms
- APT is being extended
 - ▶ Optimize implementation
 - ▶ Regular expressions
 - ▶ Step reachability graph
 - ▶ Graphical user interface
 - ▶ ...

Philosophers: [▶ Philosophers](#) [▶ Time](#)

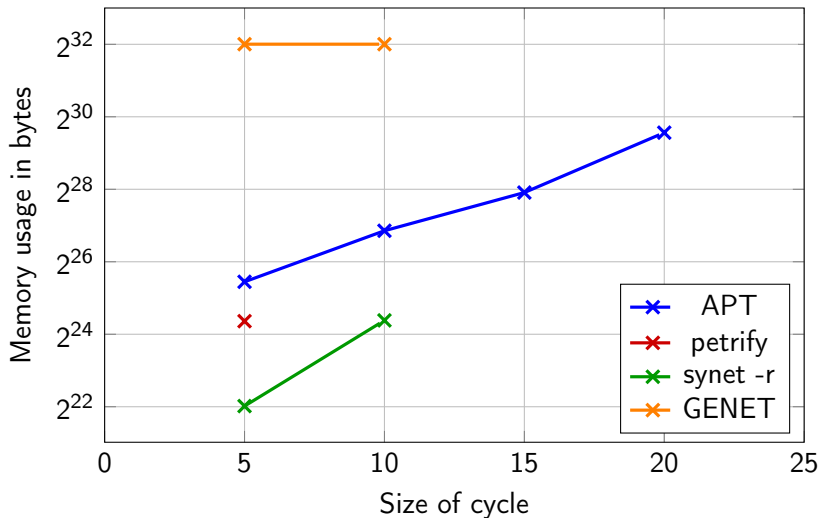
Memory usage: [▶ Cycles \$k = 1\$](#) [▶ Cycles \$k = 5\$](#) [▶ Bistate philosophers](#)

Synthesizing cycles with $k = 1$ tokens⁶: Memory usage



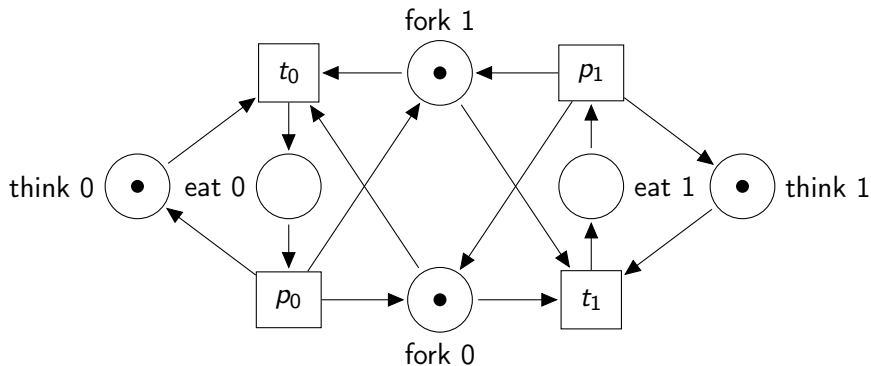
⁶Upper time limit: 600 sec

Synthesizing cycles with $k = 5$ tokens⁷: Memory usage

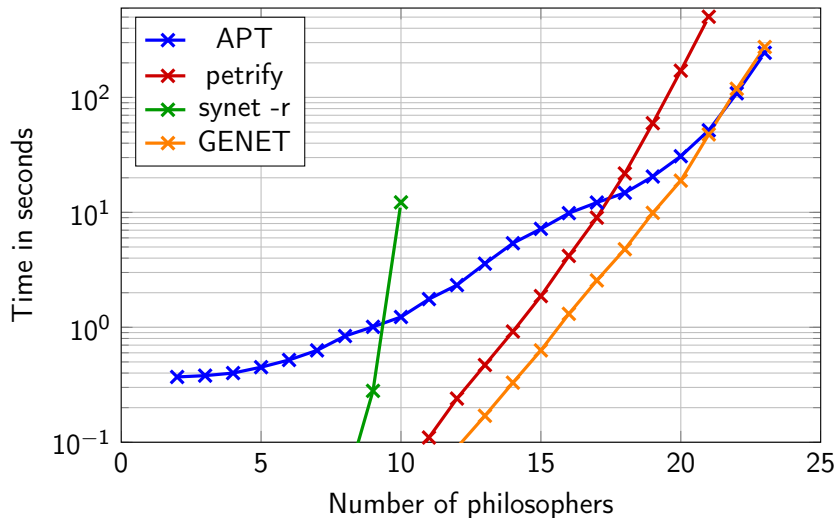


⁷Upper time limit: 600 sec

Two-state dining philosophers with $n = 2$

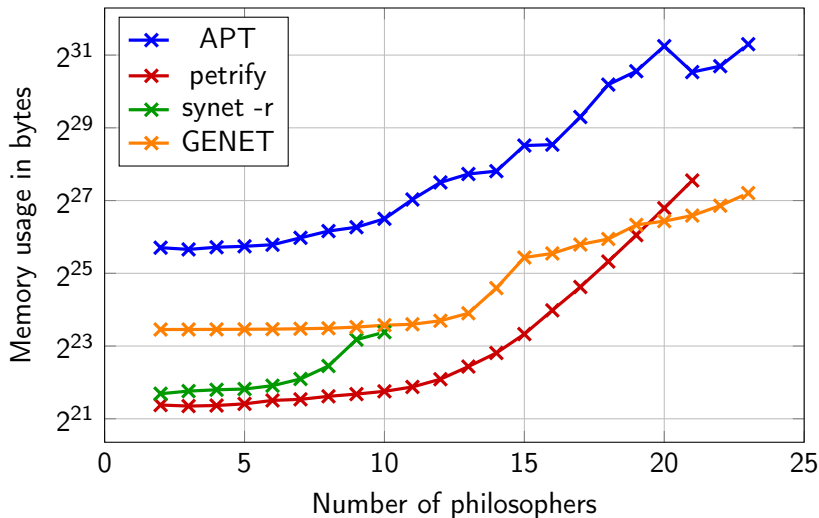


Synthesizing two-state dining philosophers⁸: Run time



⁸Upper time limit: 600 sec

Synthesizing two-state dining philosophers⁹: Memory usage



⁹Upper time limit: 600 sec